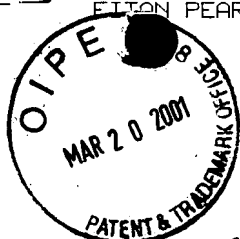


S/N: 09/357,235

RECEIVED **PATENT**
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
 MAR 23 2001

Applicant: Gureshnik et al.

Examiner: J. Jeanglande

TC 2000 MAIL ROOM
2819

Serial No.: 09/357,235

Group Art Unit:

Filed: July 20, 1999

Attorney Docket No.: P-2588-US

Title:

PULSE MODULATED DIGITAL TO ANALOG CONVERTER (DAC)

 #9/a
 3/27/01
 Surles
AMENDMENT
 Assistant Commissioner of Patents
 Washington, D.C. 20231

Sir:

This paper is submitted in response to the Office Action mailed September 22, 2000. A request for a three month extension of time is attached hereto. However, in the event that the request has been lost and is not attached, please consider this to be a formal request for a three month extension to respond to the office action, and charge any fees associated with this paper to deposit Account No. 05-0649(EPL&C).

Please amend the above-identified patent application as follows:

In the Specification:

Please delete the section "SUMMARY OF THE INVENTION" beginning at page 4, line 1 and ending at page 6, line 13.

Please replace the paragraph beginning at page 7, line 6, with the following rewritten paragraph:

Fig. 1 is a schematic illustration of a portion of an integrated circuit having a DAC of an embodiment of the present invention implemented in it;

Please replace the paragraph beginning at page 7, line 13, with the following rewritten paragraph:

Fig. 3 is a schematic illustration of the digital converter of Fig. 1, according to an embodiment of the present invention;